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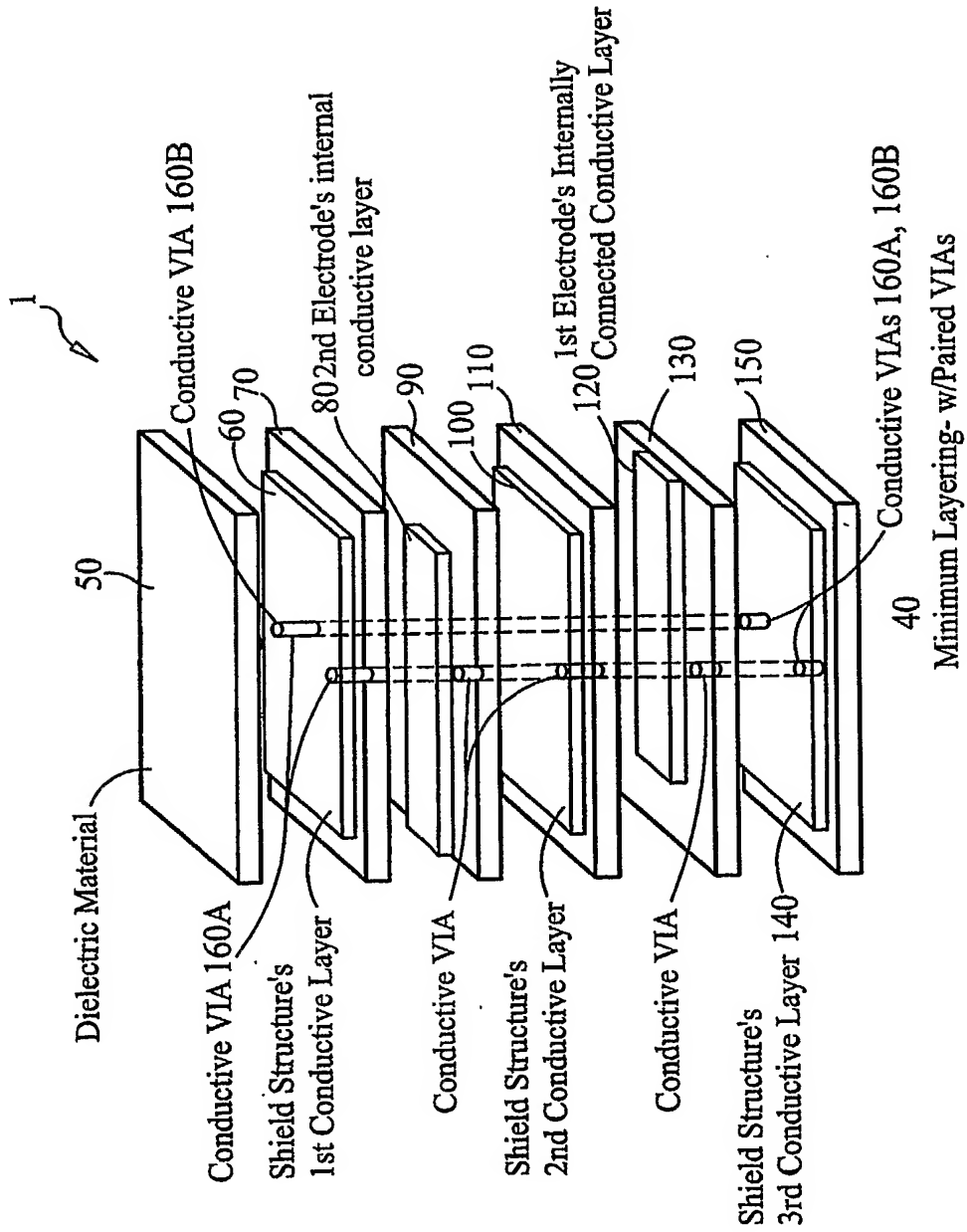


FIG. 1A

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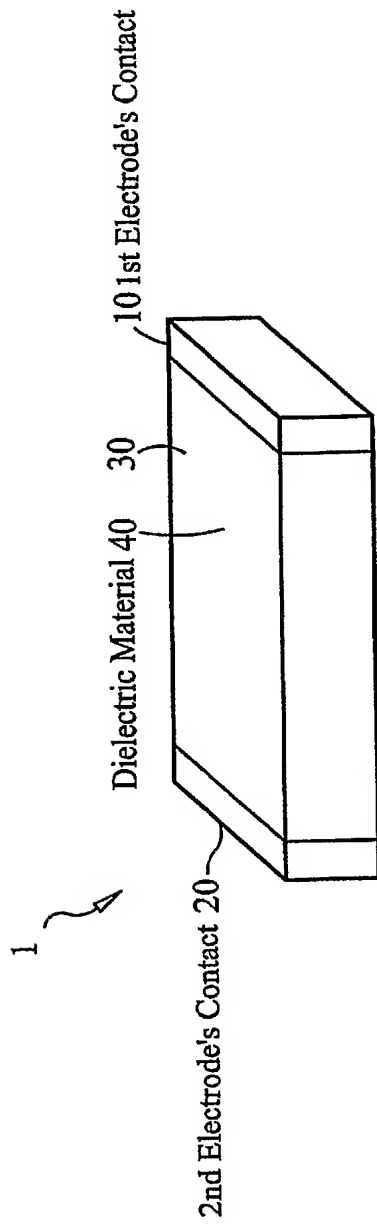


FIG. 1B

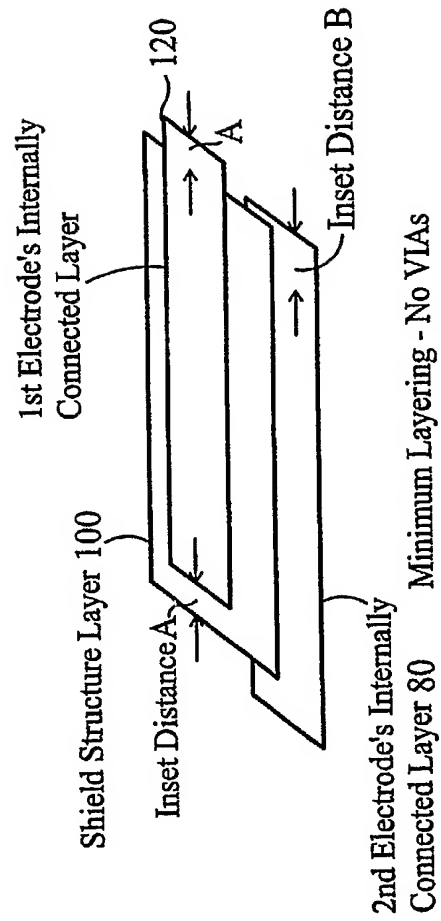


FIG. 1C

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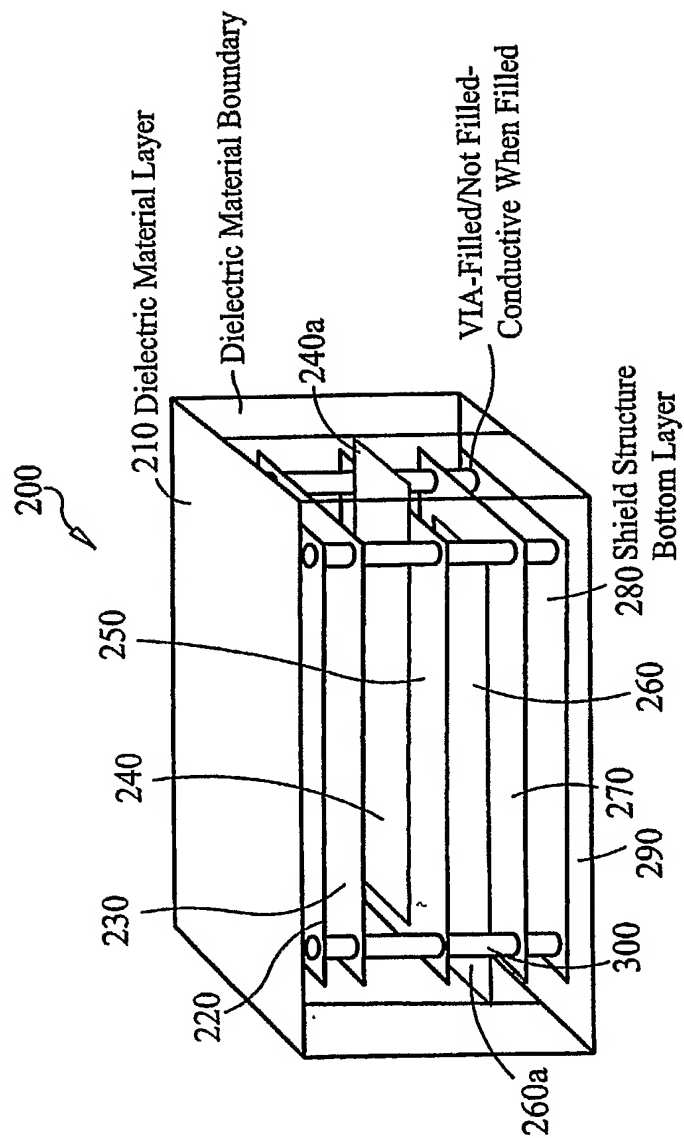


FIG. 2A

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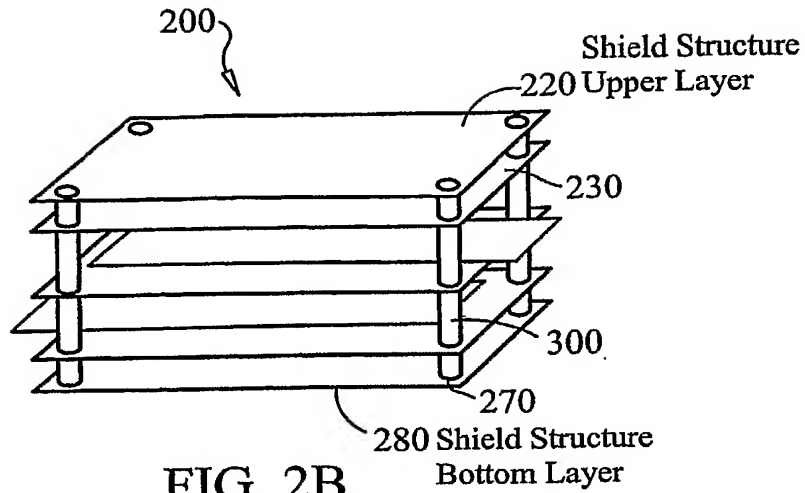


FIG. 2B

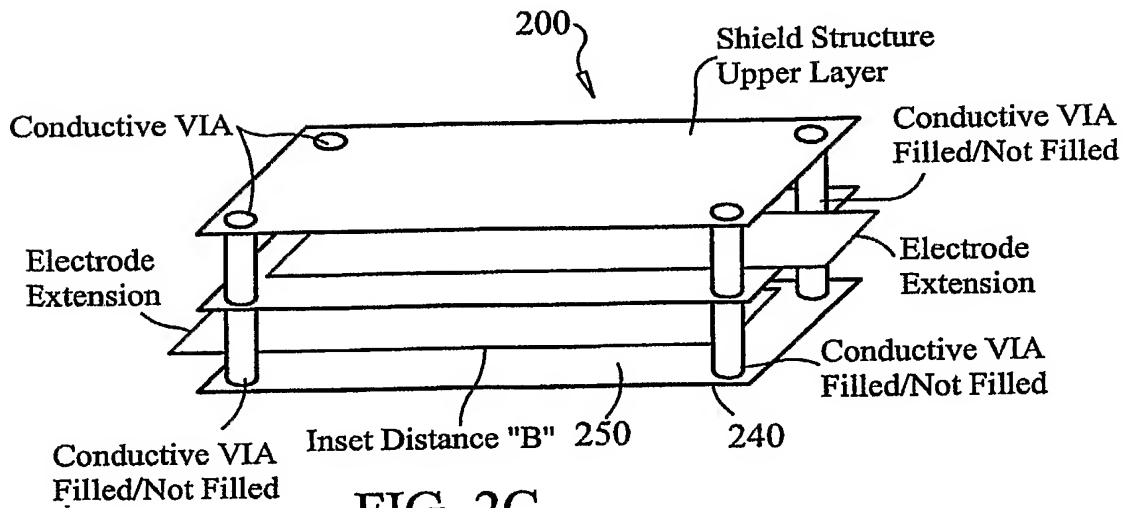
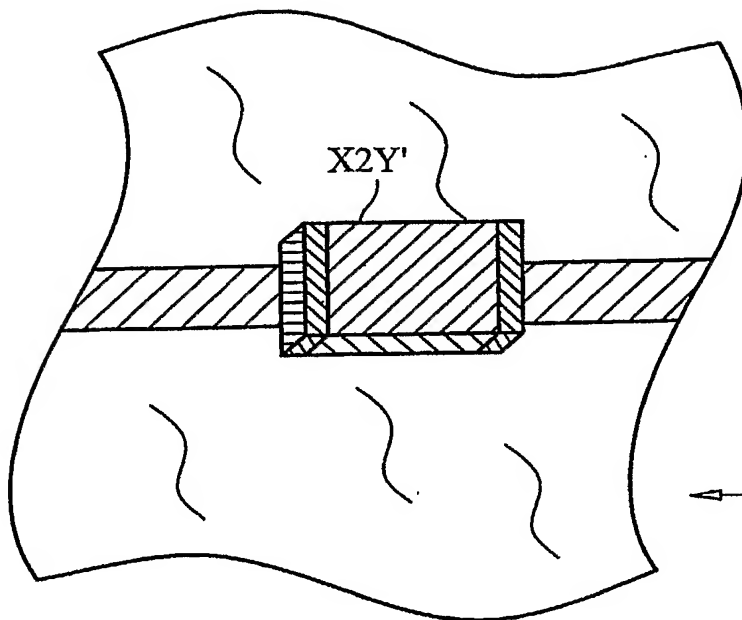


FIG. 2C

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perspective  
views  
↓

FIG. 3A

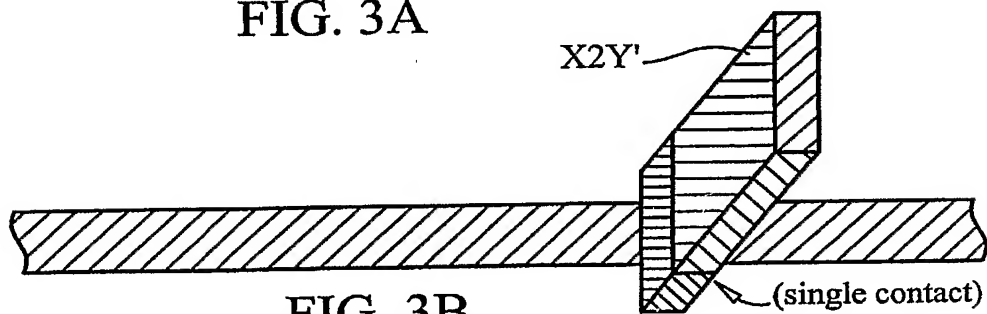


FIG. 3B

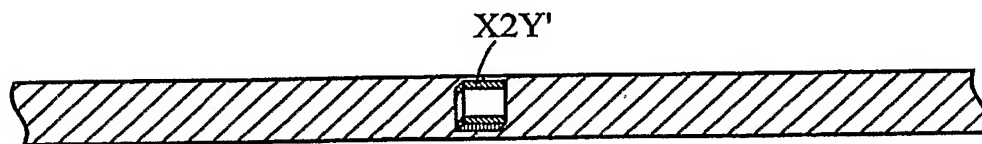


FIG. 3C

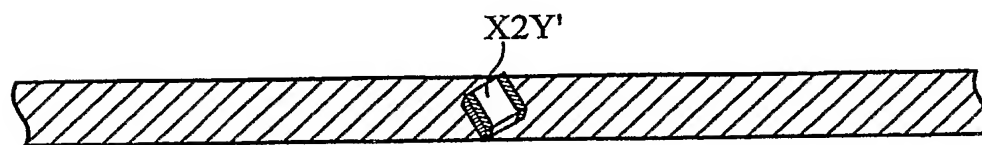
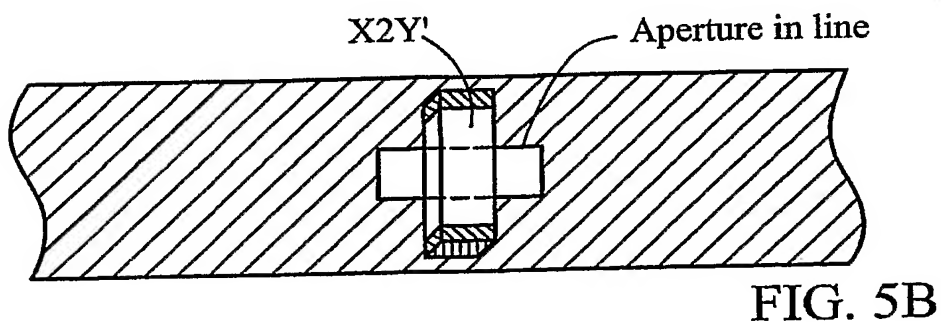
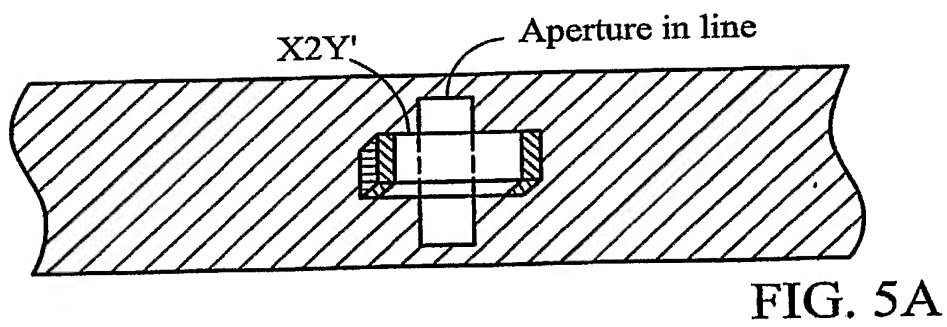
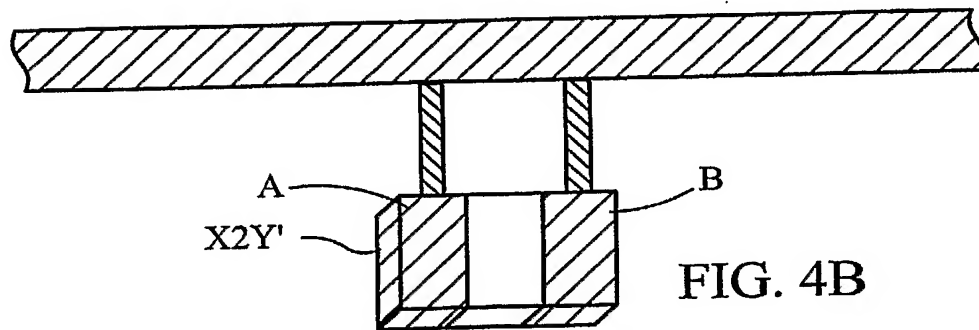
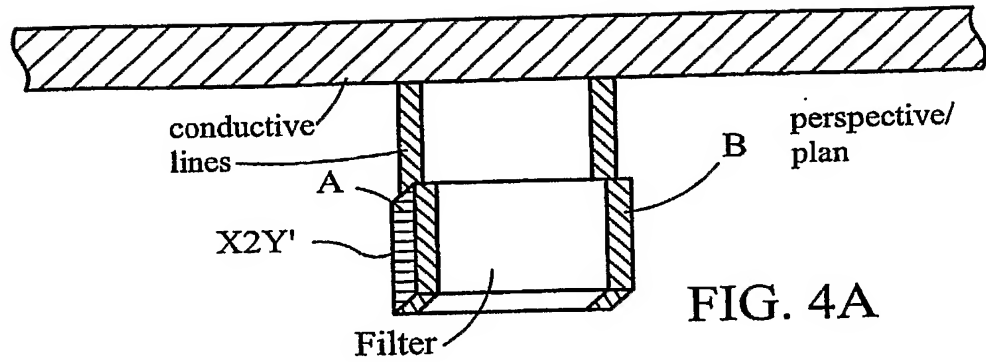


FIG. 3D

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## Filter Arrangement

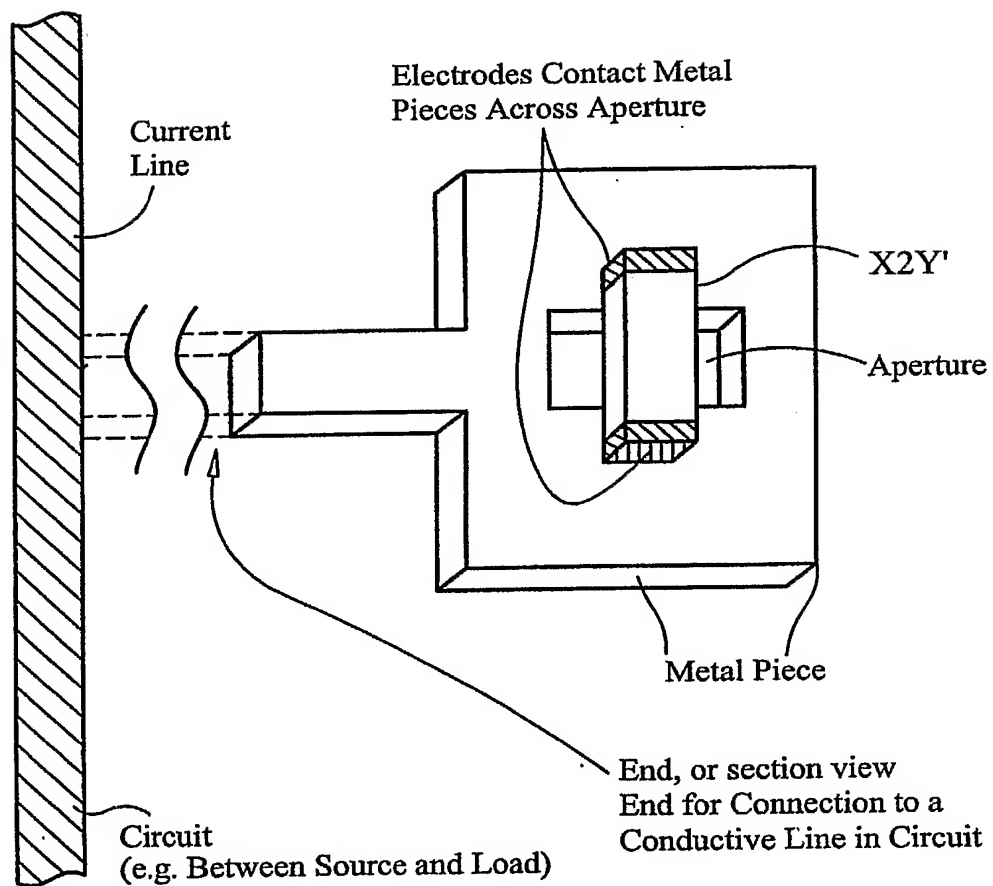


FIG. 6

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Alternative Filter Arrangement

Plan View

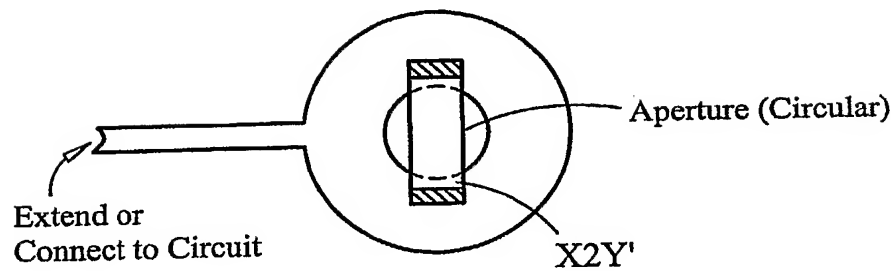


FIG. 7

Plan View

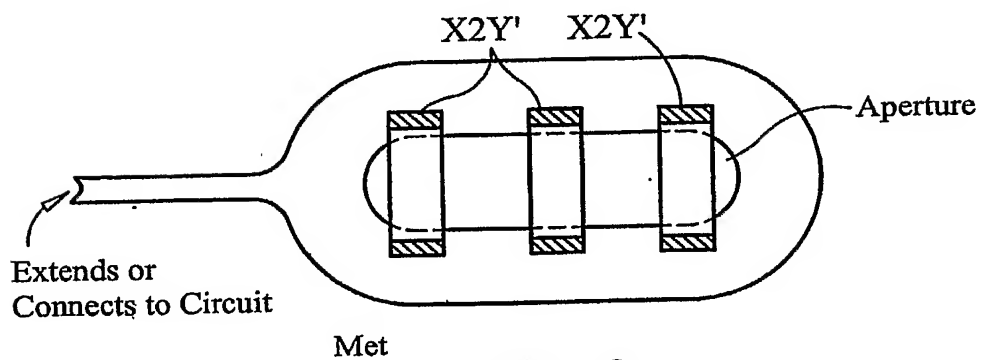


FIG. 8



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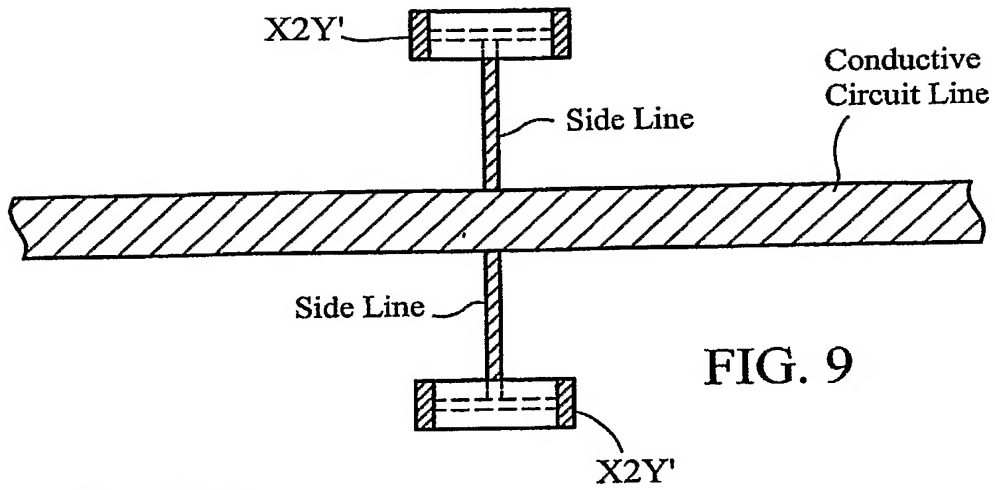


FIG. 9

Plan View

PC or IC

Various X2Y' Arrangements

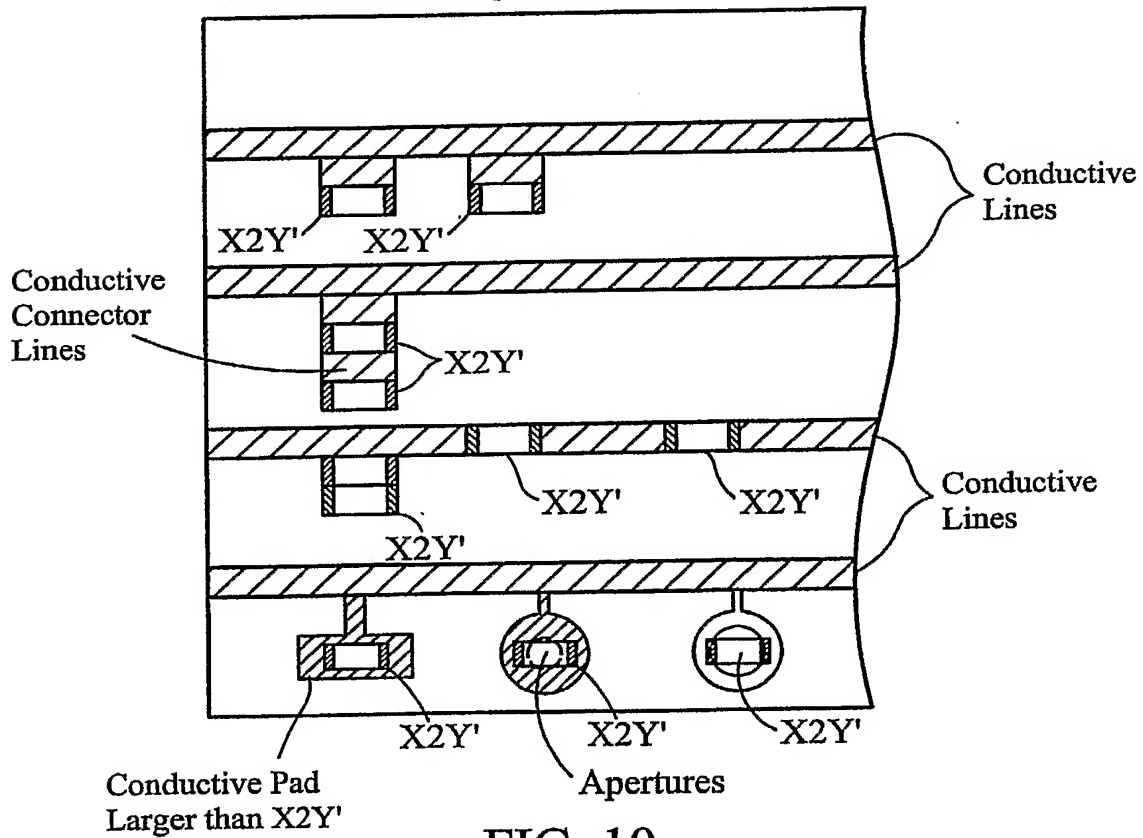


FIG. 10

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# Plan View, Partial Section

PC board or CPU with multiple lines or waveguides for "high frequency" operation.

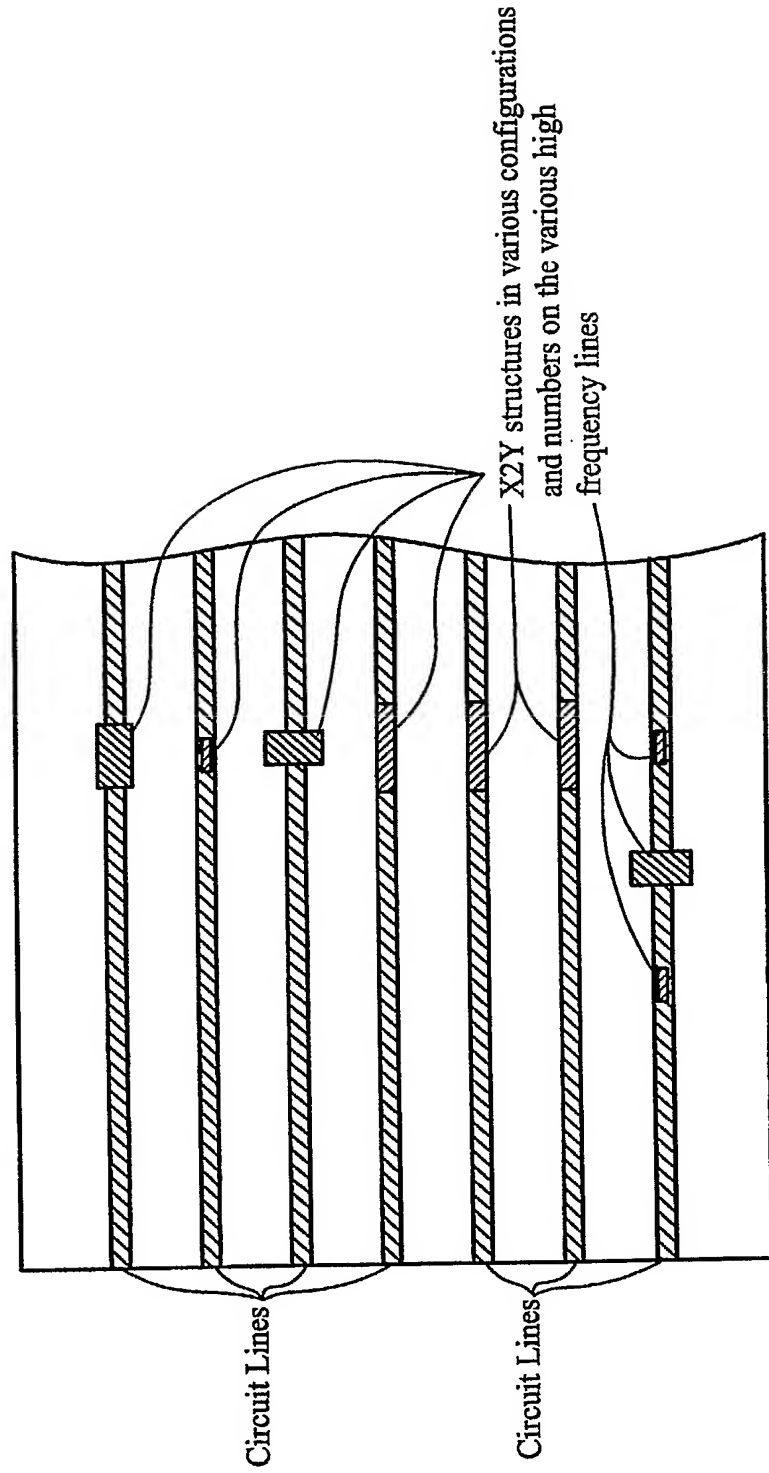
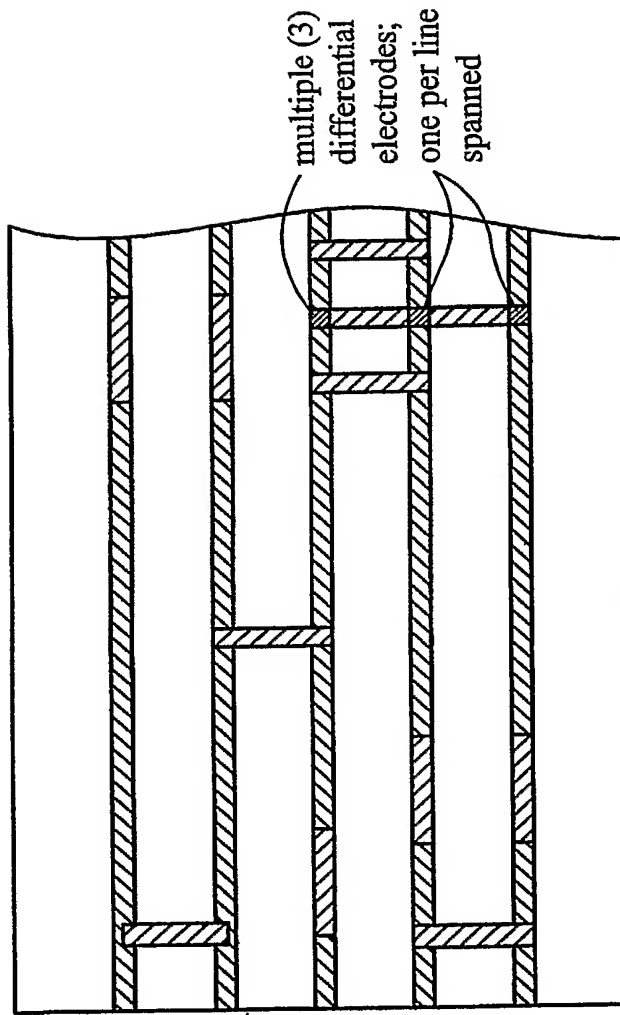


FIG. 11

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## Plan View, Partial Section

PC board or CPU with multiple lines or waveguides  
for "high frequency" operation.



X2Y' structure in various configurations  
and number; some spanning at least 2 lines.  
Preferably, one differential electrode per  
line spanned.

FIG. 12

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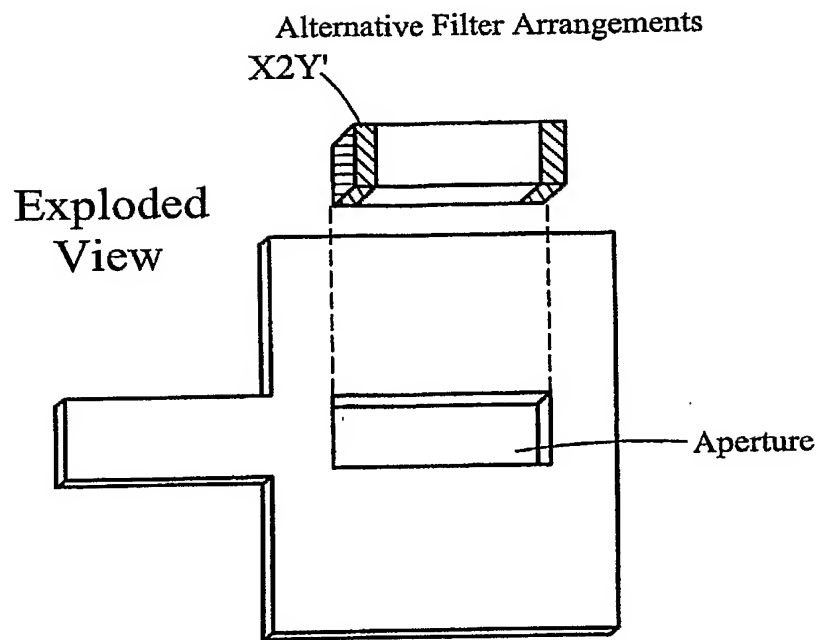


FIG. 13A

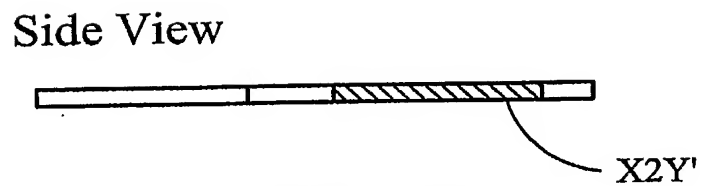


FIG. 13B

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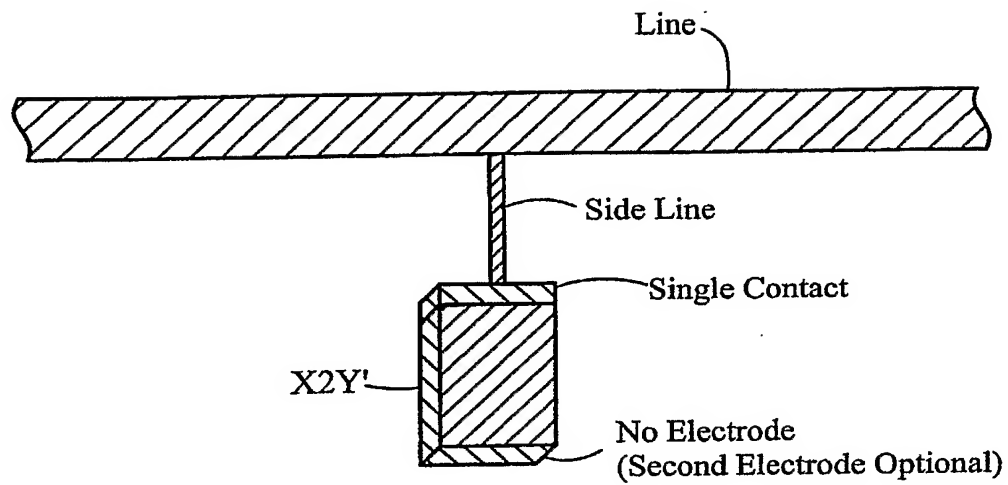


FIG. 14

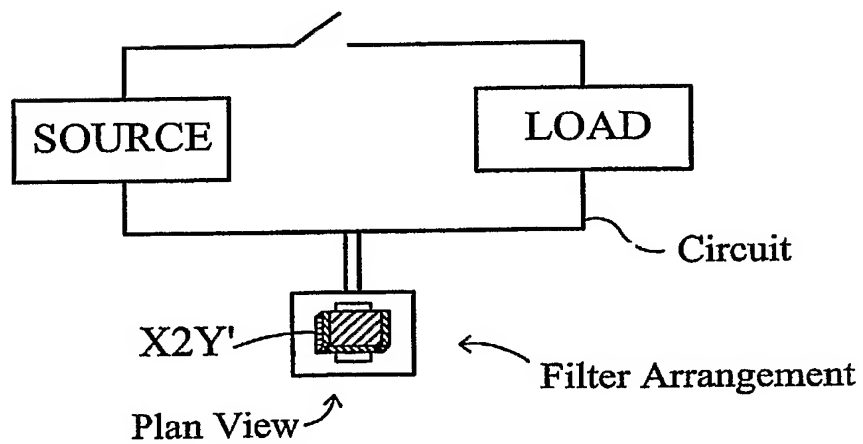
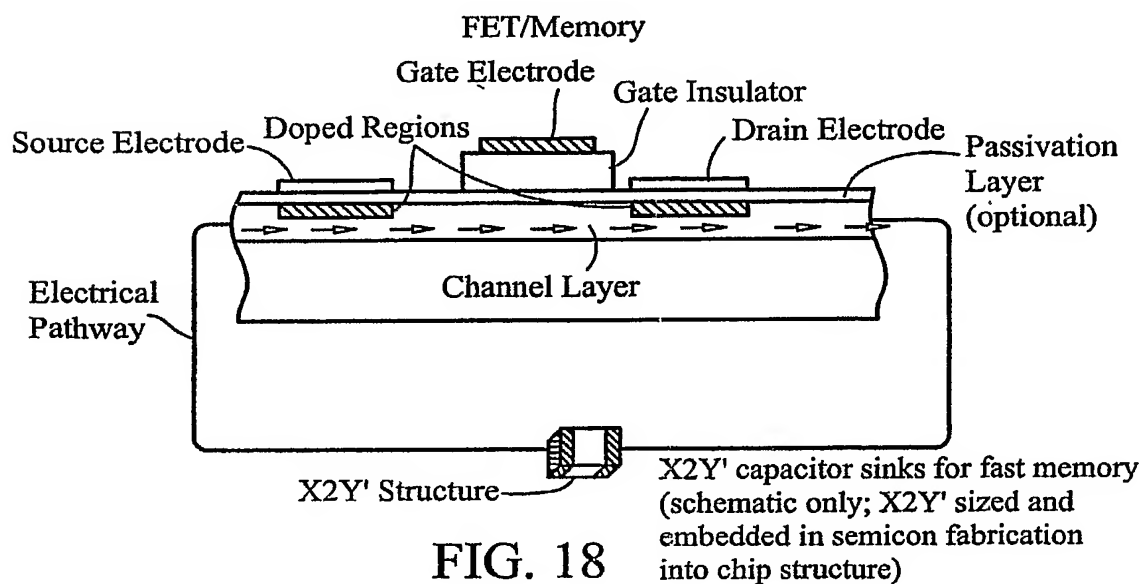
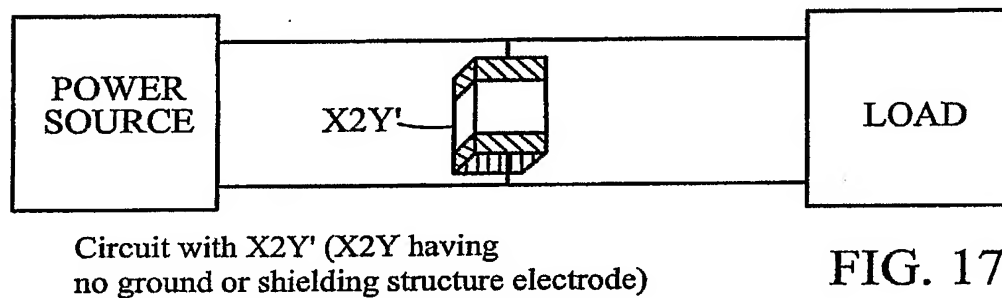
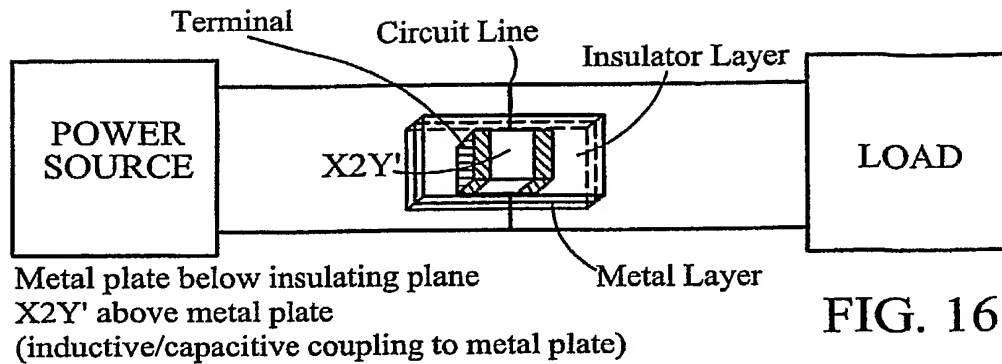


FIG. 15

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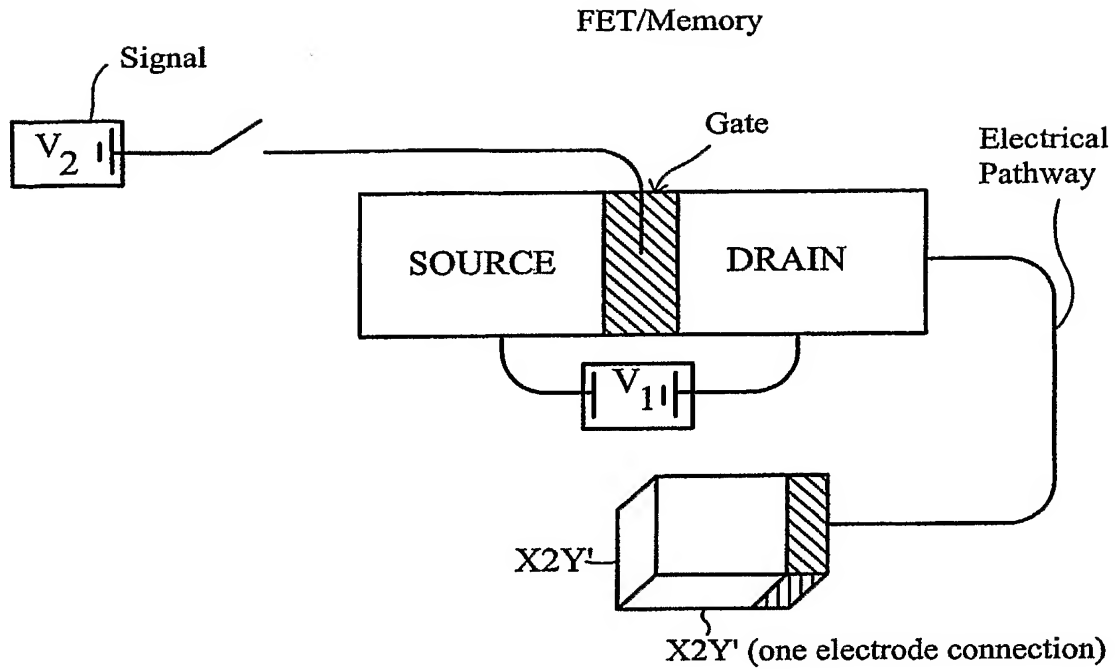


FIG. 19A

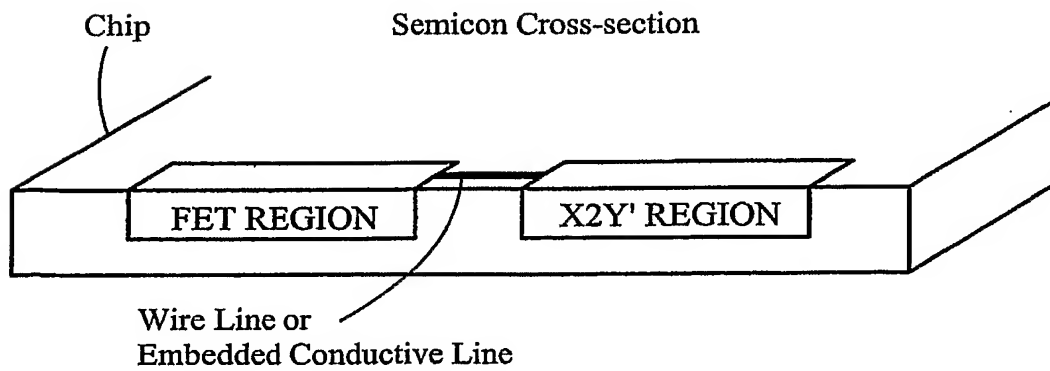


FIG. 19B

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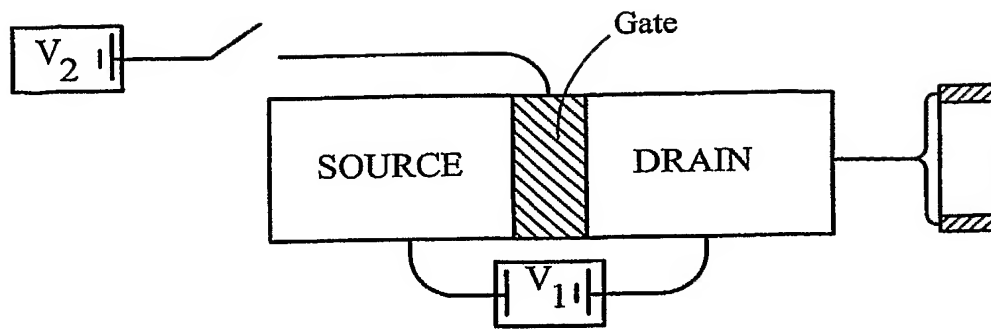


FIG. 20A

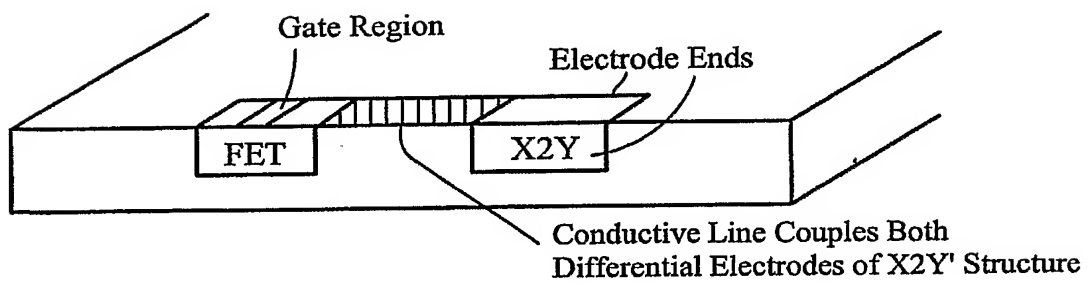


FIG. 20B



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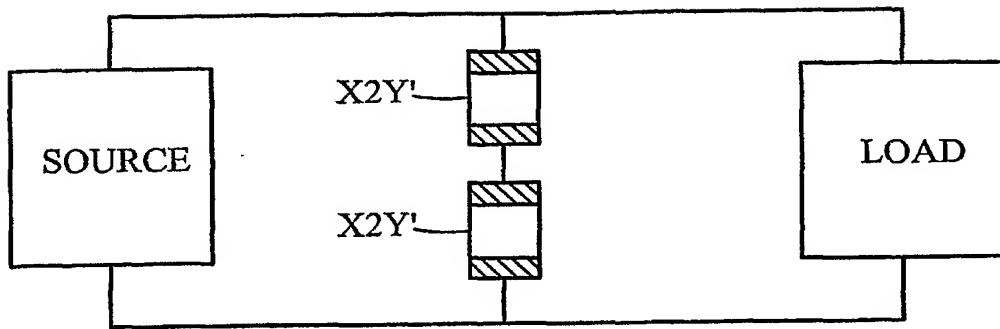


FIG. 21

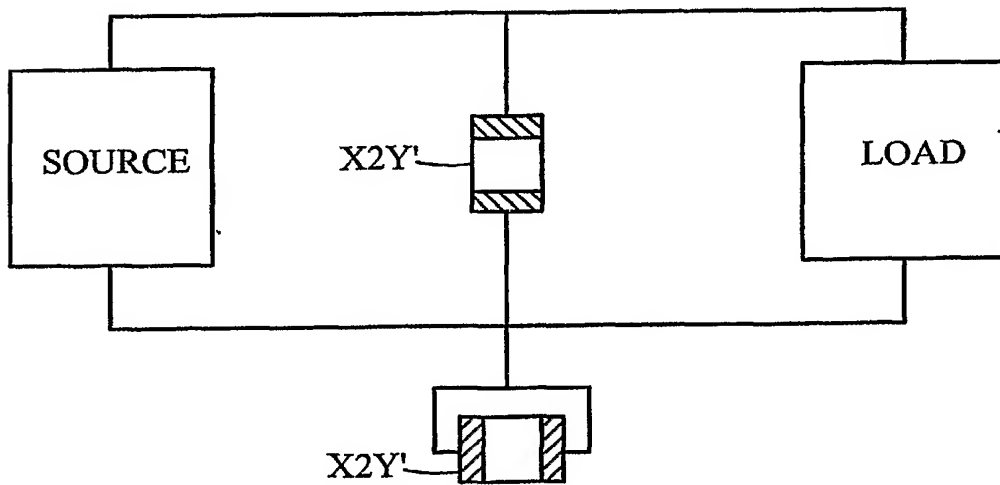


FIG. 22

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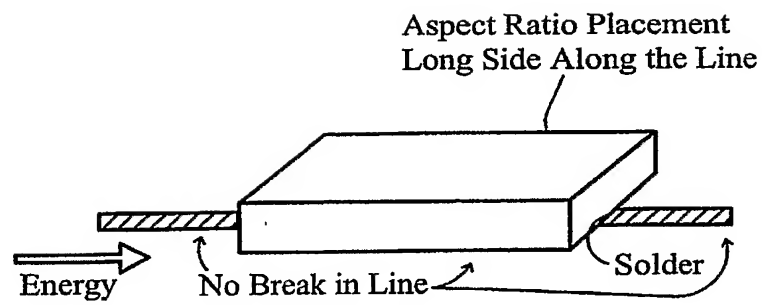


FIG. 23A

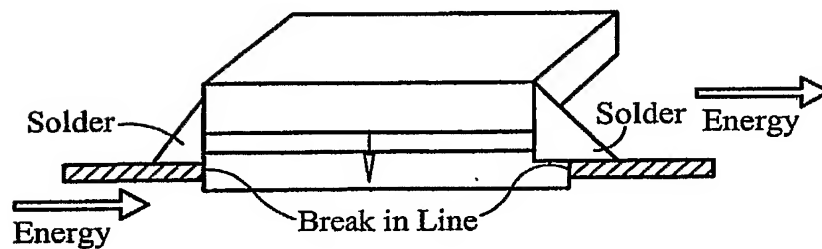


FIG. 23B

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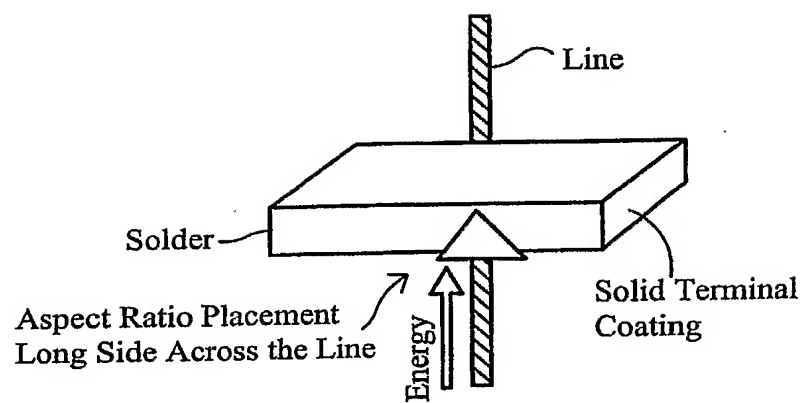


FIG. 23C

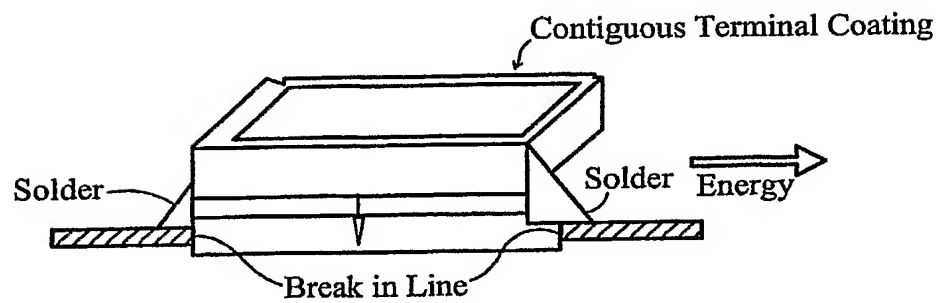
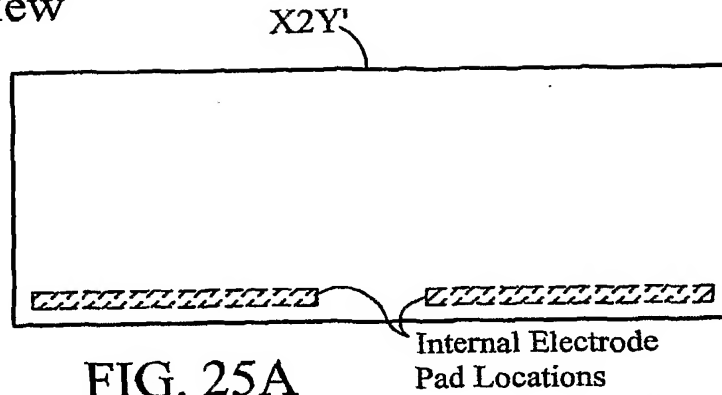


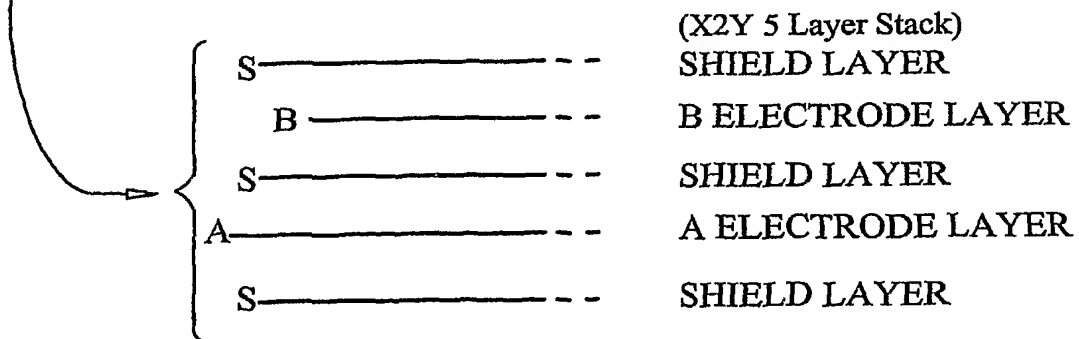
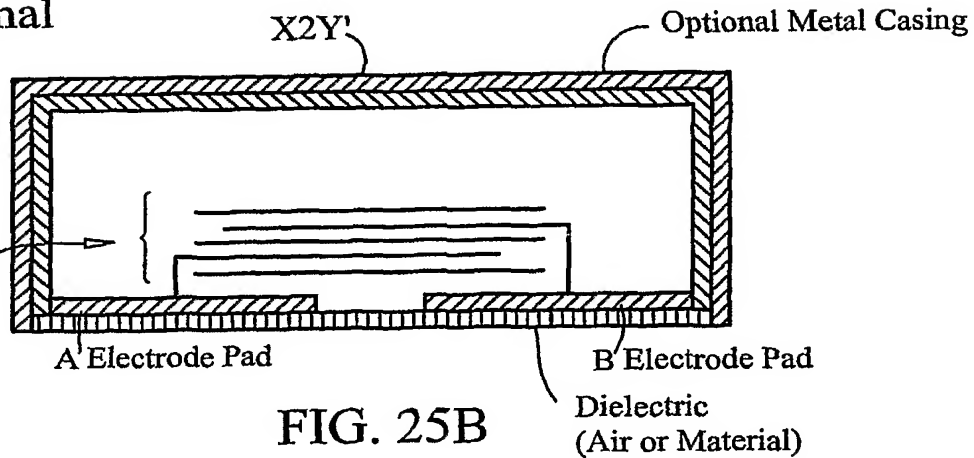
FIG. 24

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Side View



Side  
Sectional  
View



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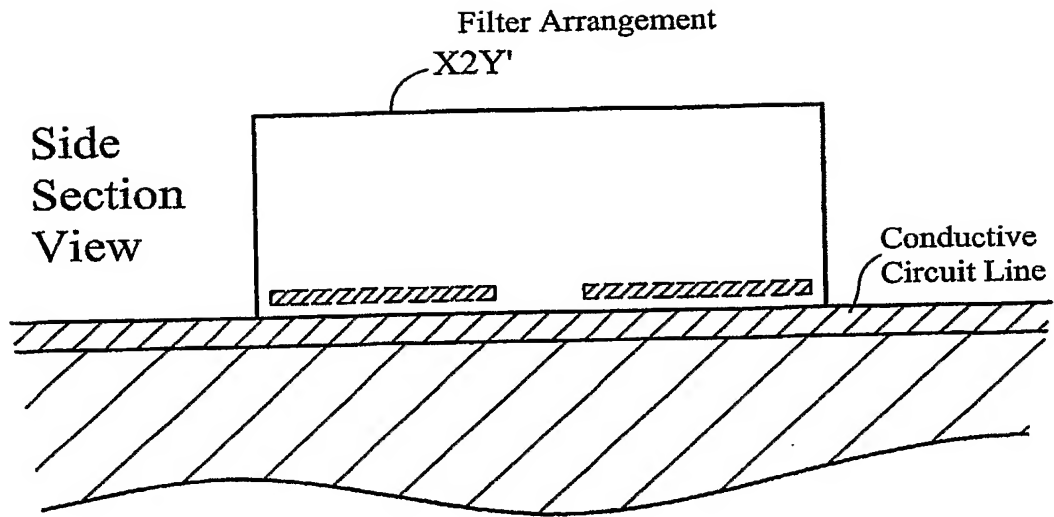


FIG. 26A

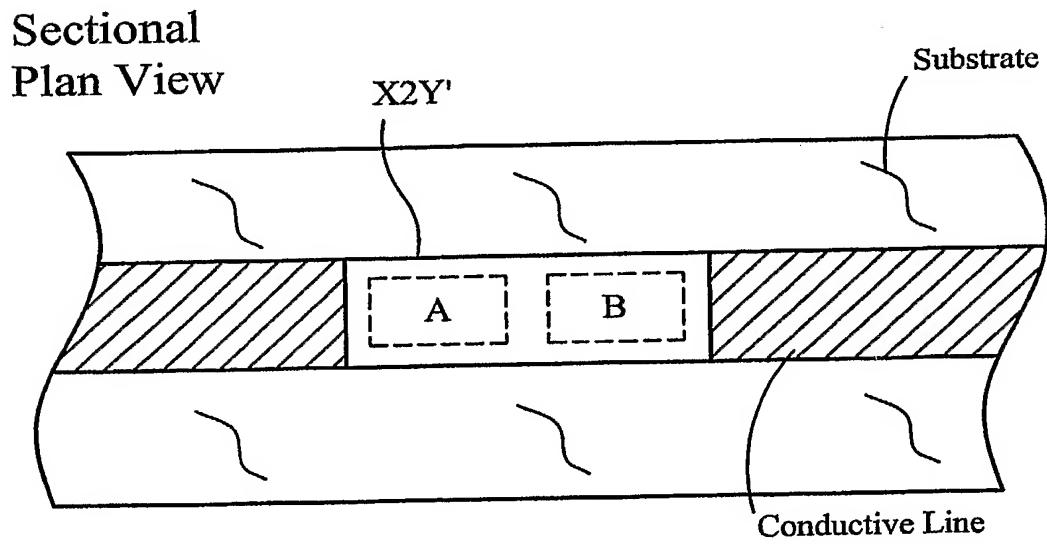


FIG. 26B

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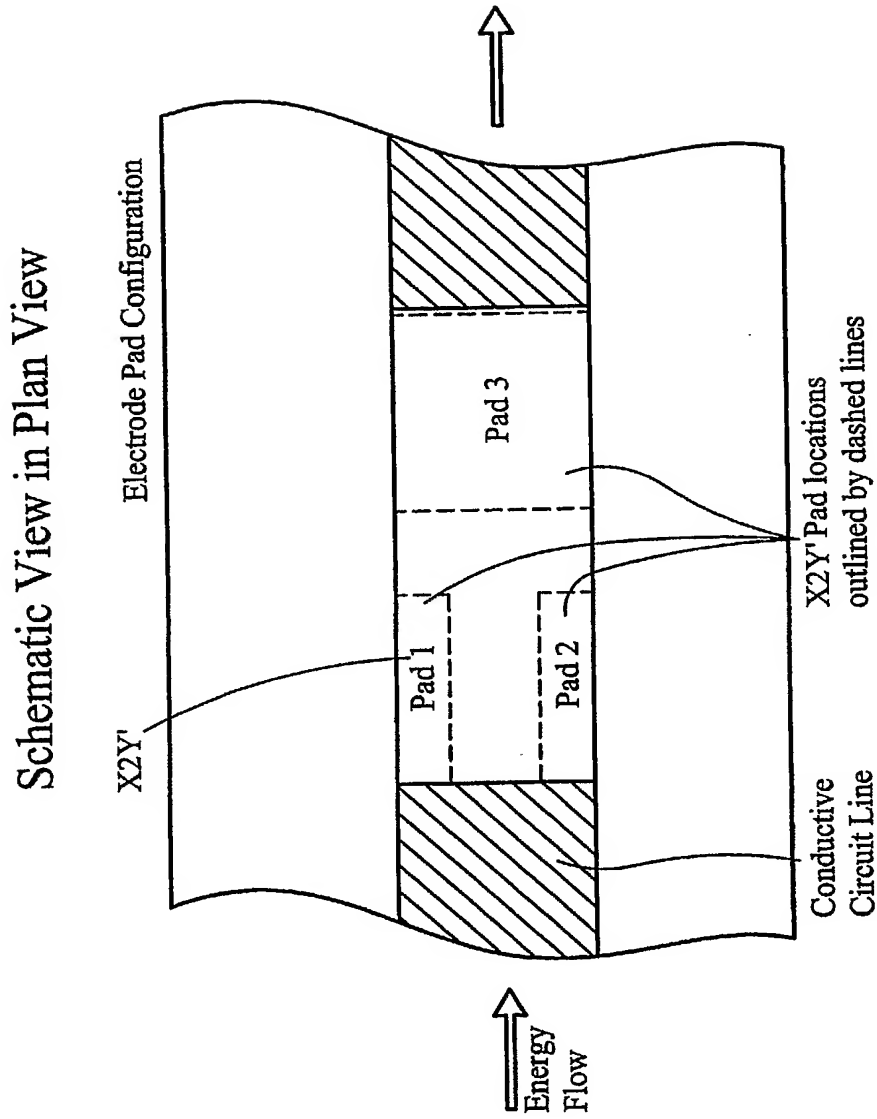


FIG. 27